9P 1941

CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8) Docket No.			
Applicant(s): ROBERT K. RIFFEE		RECEIVED	87552.97R074/CSD-55
Serial No.	Filing Date	OCT 20 Examiner	Group Art Unit
08/800,574	MARCH 18, 1997	INOLOGY CENTER 1700	
Invention: NARROWB	AND VIDEO CODEC		
OIPE			
OCT 1 8 2000) 		
I hereby certify that this	s Power of Attorney	(Hawiif, Ama of company and avec)	
is being deposited wit	th the United States Postal Sen	(Identify type of correspondence) vice as first class mail in an	envelope addressed to: The
-			
Assistant Commission	er for Patents, Washington, D.C	. 20231 on <u>October</u>	(6, 2000 <u> </u>
			L d
		Dames D. T.	
		Penny R. T (Typed or Printed Name of Person	
		Vm	
		(Signature of Person Mailin	g Correspondence)
	N. C. I.	14	
	Note: Lach paper must na	ve its own certificate of mailing.	
			.••

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTORNEY

Docket No. 87552.97R017

Name of Applicant:

Intersil Cor

Address of Applicant:

2401 Palm Bay ROCK

Mail Stop: 53-209

Palm Bay, FL 32905

Title:

(See Attached Listing)

Serial No., if Any:

(See Attached Listing)

Filed:

(See Attached Listing)

TO THE ASSISTANT COMMISSIONER FOR PATENTS

The Assistant Commissioner for Patents Washington, D.C. 20231

Honorable Sir:

I hereby appoint:

Stephen J. Sand

Douglas A. Balog Reg. No. 42,288 Bidyut K. Niyogi Reg. No. 27, 071 Reg. No. 41,615 Paul A. Bernkopf Laurence S. Roach Reg. No. 45,044 Reg. No. 34,716

as principal attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Please direct all future correspondence to:

Bv:

Thomas R. FitzGerald, Esq.

Reg. No. 26,730

JAECKLE FLEISCHMANN & MUGEL, LLP

39 State Street

Rochester, New York 14614

Tel: (716) 262-3640 Fax: (716) 262-4133

Thomas R. FitzGerald,

Dated:

October 5, 2000



•		
•		010
		E 1 8 mm 0
		INTERSIL CORPORATIONAL OFFICE
		INTERSIL CORPORATION
SERIAL NO.	FILED DATE	INTERSIL CORPORATIONAL AND DIAMOND SUBSTRATE HAVING A CARDON TO CH LCON TRANSITION LAYER AND ARRABATING THEREOF
08/190,998	2/3/1994	METHOD FOR PROVIDING A SILICON AND DIAMOND SUBSTRATE HAVING A CARBON TO SILICON TRANSITION LAYER AND APPARATUS THEREOF
08/587,953	1/17/1996	METHOD FOR PROVIDING A SILICON AND DIAMOND SUBSTRATE HAVING A CARBON TO SILICON TRANSITION LAYER AND APPARATUS THEREOF
08/483,691	6/7/1995	HIGH EFFICIENCY POWER MOS SWITCH
08/613,381	3/7/1996	CIRCUIT FOR DRIVING AN ELECTROLUMINESCENT LAMP
08/483,692	6/7/1995	PILOT TRANSISTOR FOR QUASI-VERTICAL DMOS DEVICE
08/722,354	9/27/1996	SWITCHING AMPLIFIER CLOSED LOOP DUAL COMPARATOR MODULATION TECHNIQUE
08/269,470	6/30/1994	HIGH SPEED A/D CONVERTER AND SLEW CONTROLLED PULSE DETECTOR
08/666,258	6/20/1996	RADIATION HARDENED DIELECTRIC FOR EEPROM
09/061,602	4/16/1998	RADIATION HARDENED DIELECTRIC FOR EEPROM
08/673,396	6/28/1996	HIGH VOLTAGE PROTECTION CIRCUITS
08/474,559	6/7/1995	HIGH EFFICIENCY QUASI-VERTICAL DMOS IN CMOS OR BICMOS PROCESS
08/658,010	6/4/1996	INTEGRATED CIRCUIT AIR BRIDGE STRUCTURES AND METHODS OF FABRICATING SAME
09/199,292	11/24/1998	INTEGRATED CIRCUIT AIR BRIDGE STRUCTURES AND METHODS OF
	2424225	FABRICATING SAME
08/800,574	2/18/1997	NARROWBAND VIDEO CODEC ASYMMETRIC SNUBBER RESISTOR
08/634,371	4/18/1996 4/16/1996	TRENCH MOS GATE DEVICE
08/636,904 09/096,217	6/11/1998	TRENCH MOS GATE DEVICE
08/462,171	6/5/1995	SURFACE MOUNTABLE INTEGRATED CIRCUITS AND METHODS OF
-		FABRICATION
08/505,671	7/21/1995	FAST RECOVERY TEMPERATURE COMPENSATED REFERENCE SOURCE
08/497,404	6/30/1995	SEMI-INSULATING WAFER INTEGRATED CIRCUIT WITH AN IMPROVED INDUCTOR STRUCTURE AND
08/637,132	4/24/1996	METHOD OF FABRICATION
08/650,762	5/20/1996	PRE-BOND CAVITY AIR BRIDGE
08/462,876	6/5/1995	SYSTEM FOR INTERCONNECTING STACKED INTEGRATED CIRCUITS
08/586,613	1/16/1996	METAL OXIDE SEMICONDUCTOR CONTROLLED THYRISTOR WITH AN ON- FIELD EFFECT TRANSISTOR IN A TRENCH
08/336,768	11/9/1994	METHOD FOR MAKING OHMIC CONTACT TO LIGHTLY DOPED ISLANDS FROM A SILICIDE BURIED LAYER AND APPLICATIONS
08/810,127	2/25/1997	METHOD FOR MAKING OHMIC CONTACT TO LIGHTLY DOPED ISLANDS
08/505,695	7/21/1995	FROM A SILICIDE BURIED LAYER AND APPLICATIONS AUTOMATIC FAULT MONITORING SYSTEM AND MOTOR CONTROL SYSTEM
08/461,951	6/5/1995	INCORPORATING SAME METHOD OF BONDING WAFERS HAVING VIAS INCLUDING CONDUCTIVE
,	0/3/1993	MATERIAL
08/461,037	6/5/1995	INTEGRATED CIRCUIT WITH COAXIAL INSOLATION AND METHOD
08/042,299	4/2/1993	DIAMOND INSULATOR DEVICES AND METHOD OF FABRICATION DIAMOND INSULATOR DEVICES AND METHOD OF FABRICATION
08/471,759	6/6/1995	METHOD FOR FABRICATING A POWER DEVICE AND APPARATUS THEREOF
08/662,118 09/330,437	6/12/1996 6/11/1999	METHOD FOR FABRICATING A POWER DEVICE AND APPARATUS THEREOF
08/637,937	4/23/1996	PROCESS OF FORMING TRENCH ISOLATION DEVICE
09/283,530	4/1/1999	PROCESS OF FORMING TRENCH ISOLATION DEVICE
08,650,688	5/20,1996	INTEGRATED CIRCUIT WITH AN AIR BRIDGE HAVING A LID
08/771,944	12/23/1996	DEEP TRENCH ETCH ON BONDED SILICON WAFER
09/266,066	3/10/1999	DEEP TRENCH ETCH ON BONDED SILICON WAFER
08/481,115	6/7/1995	DEFECT CONTROL IN FORMATION OF DIELECTRICALLY ISOLATED
07/939,786	9/3/1992	SEMICONDUCTOR DEVICE REGIONS BONDED WAFER PROCESSING WITH METAL SILICIDATION
08/351,933	12/8/1994	BONDED WAFER PROCESSING WITH METAL SILICIDATION
08/915,841	8/21/1997	BONDED WAFER PROCESSING WITH METAL SILICIDATION
09/316,580	5/21/1999	BONDED WAFER PROCESSING WITH METAL SILICIDATION
08/973,769	7/27/1998	MONOLITHIC CLASS C AMPLIFIER
09/392,806	9/9/1999	MONOLITHIC CLASS C AMPLIFIER
08/646,471	5/8/1996	SEMICONDUCTOR DEVICE WITH DOPED SEMICONDUCTOR AND DIELECTRIC TRENCH SIDEWALL LAYERS
08/705,536	8/29/1996	LID WAFER BOND PACKAGING AND MICROMACHINING
09/073,776	5/6/1998	LID WAFER BOND PACKAGING AND MICROMACHINING
08/671,243	6/27/1996	INTEGRATED CIRCUIT CONTAINING DEVICES DIELECTRICALLY ISOLATED AND JUNCTION ISOLATED FROM SUBSTRATE
08/310,280	9/21/1994	PROGRAMMABLE ELEMENT IN BARRIER METAL DEVICE AND METHOD
08/586,556	1/16/1996	METHOD AND APPARATUS FOR RADIATION HARDENED ISOLATION
•		

### ### ##############################				
207464,053 315/1999 ELECTROSTATIC DISCLAGGIF PROTECTION DEVICE GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 2071994 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08797,373 72/1997 100	7			
207464,053 315/1999 ELECTROSTATIC DISCLAGGIF PROTECTION DEVICE GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 2071994 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08797,373 72/1997 100	, .	,		
207464,053 315/1999 ELECTROSTATIC DISCLAGGIF PROTECTION DEVICE GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 2071994 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08797,373 72/1997 100				
207464,053 315/1999 ELECTROSTATIC DISCLAGGIF PROTECTION DEVICE GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 2071994 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08494,181 67/1995 GRADED COLLISTOR PIOL INDICTIVE LOADS 08797,373 72/1997 100			•	
2072-64-05 315/1999 ELECTROSTATIC DISCHARGE PROTECTION DEVICE 475/1911 308/1915/33 20.5/1994 308/1915/33 20.5/1994 308/1915/33 20.5/1994 308/1915/34		08/596.079	2/6/1996	ELECTROSTATIC DISCHARGE PROTECTION DEVICE
087919.63 27/1994 GRADED COLLETTOR FOR RIDDUTTIVE LOADS				
98355,688 121447994 HOME VIDEO CONTREENTING SYSTEM (HAS) 98493,136 671995 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (98790,793 1271997) 99735,134 6717999 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99735,134 6717999 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99735,134 6717999 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99735,134 6717999 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99735,134 6717996 LICCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99737,171 998 SILENT START CLASS DAMPLIFIER SYSTEM AND AMERICAN PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS (1987) 99736,100 9971 997 DOUBLE DIFFERENT COMPANIEST SYSTEM CHARLEST SYSTEM			3/25/1991	
08484,116 (671995 NTEGRATED CIRCUIT WITH EDGE CONNECTIONS AND METHOD POR 9779979) 27121997 NTEGRATED CIRCUIT WITH THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THIN PLAN RESISTORS WITH DIFFERENCE COMPOSITIONS SILENT START CLASS DE PORTUGA PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THE PATERNING THIN PLAN RESISTORS AND AMETHOD POR CO-PATERNING THIN PLAN RESISTOR THE PATERNING THIN PLAN RESISTOR THE PATERNING TH		08/191,963	2/3/1994	GRADED COLLECTOR FOR INDUCTIVE LOADS
MARCH MARC		08/356,683	12/14/1994	HOME VIDEO CONFERENCING SYSTEM (HAS)
OFF 1997 121997				INTEGRATED CIRCUIT WITH EDGE CONNECTIONS AND METHOD
PATTERNING THIN FILM RESISTORS WITH DIFFERENT COMPOSITIONS				LOCAL OXIDATION PROCESS FOR HIGH FIELD THRESHOLD APPLICATIONS
PATTERNING THIN FIRM RESISTORS WITH DIFFERENT COMPOSITIONS		08/799,793	2/12/1997	PATTERNING THIN FILM RESISTORS WITH DIFFERENT COMPOSITIONS
Section Sect		09/335,134	6/17/1999	PATTERNING THIN FILM RESISTORS WITH DIFFERENT COMPOSITIONS
08671,377 06781996 DOUBLE DIFFUSED MOS DEVICE AND METHOD				
087726,659 10771996 SURFACE MOUNT DIE BY HANDLE REPLACEMENT 086066,355 371/1993 INTEGRATED CIRCUIT METHOD FOR AND STRUCTURE WITH NARROW LINE WIDTES 10781995 WIDTES 10781995 WIDTES 10781996 WIDTES 10781994 WIDTES 10781994 WIDTES 10781994 WIDTES 10781994 WIDTES 10781994 WIDTES 10781994 WIDTES 10781995 WIDTES 10781995 WIDTES 10781996 WIDTES 10781996 WIDTES 10781996 WIDTES		-		
09/110/212 77/1/998 SURFACE MOUNT DIE BY HANDLE REPLACEMENT 08/19/19/19/19/19/19/19/19/19/19/19/19/19/				
08/460,993 6/5/1995 INTEGRATED CIRCUIT METHOD FOR AND STRUCTURE WITH NARROW LINE WIDTHS				
WIDTIS				INTEGRATED CIRCUIT METHOD FOR AND STRUCTURE WITH NARROW LINE
WIDTHS 98/193,411 972/1993 DIE SEPARATION METHOD FOR SILICON ON DIAMOND CIRCUIT STRUCTURES 972/185,395 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION MULTICITANNEL SIGMA-DELTA AD CONVERTERS WITH IMPROVED THE MENOR		,		WIDTHS
08/125,411 9/22/1993 DIE SERRAATION METHOD FOR SILICON ON DIAMOND CIRCUIT STRUCTURES 08/18,358 8/18/1994 SILD-MICRON BONDED SOI BY TRENCH PLANARIZATION 08/785,395 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF PABRICATION 08/287,763 89/1994 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF PABRICATION 08/28/361 5/10/1995 MALGO-TO-DIGITAL CONVERTER AND METHOD OF PABRICATION 08/29,588 8/18/1994 MALGO-TO-DIGITAL CONVERTER AND METHOD OF PABRICATION 08/29,588 8/18/1995 TRENCH INSOLATION STRESS RELIEF 08/465,246 6/5/1995 TRENCH INSOLATION STRESS RELIEF 08/485,246 6/5/1995 BONDED WAFER PROCESS RECORDERATING DIAMOND INSULATOR 08/783,792 1/15/1997 BONDED WAFER PROCESS RECORDERATING DIAMOND INSULATOR 08/783,792 1/15/1997 BONDED WAFER PROCESS RECORDERATING DIAMOND INSULATOR 08/783,792 1/15/1997 BONDED WAFER PROCESS RECORDERATING DIAMOND INSULATOR 08/493,112 4/22/1995 BONDED WAFER PROCESS RECORDERATING DIAMOND INSULATOR 08/493,112 4/22/1995 BONDED WAFER PROCESSING 08/19,12/1997 BONDED WAFER PROCESSING 08/19,12/1997 BONDED WAFER PROCESSING 08/19,12/1997 BONDED WAFER PROCESSING 08/19,12/1998 BONDED WAFER PROCESSING 08/19,12/1998 BONDED WAFER PROCESSING 08/19,12/1998 BONDED WAFER PROCESSING 08/19,12/1998 BONDED WAFER PROCESSING 08/19,12/1999 BONDED WAFER PROCESSING 08/24,16/2 1/17/1996 BONDED WAFER PROCESSING 08/24,16/3 10/12/1997 BONDED WAFER PROCESSING 08/24,16/3 10/12/1997 BONDED WAFER PROCESSING 08/24,16/3 10/12/1996 BONDED WAFER PROCESS		08/460,993	6/5/1995	
DIE SEPRARATION METHOD FOR SILICON ON DIAMOND CIRCUIT STRUCTURES		08/292,482	8/18/1994	
08/108,358 8/18/1993 SUB-MICKON BONDED SOI BY TRENCH PLANARIZATION 07/785,395 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 92/09,66 10/29/1992 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION THROUGHIST THROUGHI		08/125,411	9/22/1993	
037783-395 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION		00/100 250	9/19/1003	
08/287/763 89/1994 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION				ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION
9209,66 10/29/1992 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION MULTI-CHANNEL SIGNA-DELTA AND CONVERTERS WITH IMPROVED THROUGHPUT TRENCH INSOLATION STRESS RELIEF 08/45,246 6/5/1995 08/248,844 5/25/1994 08/248,844 5/25/1994 08/248,844 5/25/1994 08/248,844 5/25/1994 08/248,844 5/25/1994 08/248,844 5/25/1994 08/248,844 5/25/1995 08/248,844 5/25/1995 08/248,844 5/25/1997 08/248,300 4/14/1997 08/24,300 4/14/1997 08/24,300 4/14/1997 08/24,300 4/14/1997 08/24,300 1/2 4/22/1995 08/24,300 1/2 4/22/1995 08/24,300 1/2 4/22/1995 08/24,300 1/2 4/22/1995 08/24,300 1/2 4/22/1995 08/24,300 1/2 4/22/1995 08/24,400 1/22/1996 08/24,400 1/22/1996 08/24,400		•		ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION
08.493,251 \$1/01/995 MULTI-CHANNEL SIGMA-DELTA AD CONVERTERS WITH IMPROVED THROUGHPUT 118.004 11				ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION
TRENCH INSOLATION STRESS RELIEF				MULTI-CHANNEL SIGMA-DELTA A/D CONVERTERS WITH IMPROVED
188465,246 6/5/1995 TRENCH INSOLATION STRESS RELIEF		00/150,551	5, 10, 17, 1	
08/405,246 65/1995 TRENCH INSOLATION STRESS RELIEF		08/292,588	8/18/1994	TRENCH INSOLATION STRESS RELIEF
08/13/950 12/4/1995 BONDED WAFER PROCESS INCORPORATING DIAMOND INSULATOR			6/5/1995	
08/733,792		08/248,844	5/25/1994	SILICON ON DIAMOND CIRCUIT STRUCTURE
08/843,302		08/513,950	12/4/1995	
08/49/312 47/8/1995 BONDED WAFER PROCESSING 08/137/293 10/14/1993 BONDED WAFER PROCESSING 07/197/635 77/20/1992 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/991/490 27/21/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/956/074 10/22/1997 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/733,368 10/17/1996 LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION 08/745,104 11/7/1996 LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION 08/741,639 10/28/1996 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION 08/301,366 9/13/1994 CONSTANT DELAY LOGIC CIRCUITS AND METHOD OF FABRICATION CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION CAPACITOR STRUCTURE IN A BONDED WAFER WITH THREE POINT CALIBRATION APPARATUS AND METHOD CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS CA		08/783,792	1/15/1997	-
10/14/1993 BONDED WAFER PROCESSING			4/14/1997	
ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION				
08/301,400 2/11/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 07/984,187 11/20/1992 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/956,074 10/22/1997 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/733,368 10/17/1996 LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION 08/741,639 10/28/1996 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION 08/305,366 9/13/1994 CONSTANT DELAY LOGIC CIRCUITS AND METHOD OF FABRICATION 08/301,105 10/28/1994 SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS NO METHOD 08/490,566 6/15/1995 EFFICIENT BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS 08/490,016 6/13/1995 BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS 08/490,952 6/13/1995 BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMP BRIVER SYSTEM 08/491,952 6/13/1995 SURFACE MOUNTABLE INTEGRATED CIRCUIT WITH CONDUCTIVE VIAS 07/921,197 7/28/1992 BONDED WAFER PROCESSING 08/287,773 8/9/1994 BONDED WAFER PROCESSING 08/287,773 8/9/1994 BONDED WAFER PROCESSING 08/297,741 9/1/1994 HIGH SPEED COMPARATOR 08/180,666 1/13/1994 VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE 07/935,765 8/26/1992 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/39,966 11/15/1994 MICHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/511,693 12/31/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/511,693 12/31/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/511,693 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/585,707 6/30/1997 METHOD FOR FORMING COMPILMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		•		BONDED WAFER PROCESSING
07/984_187 11/20/1992 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/956,074 10/22/1997 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/733,368 10/17/1996 LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION 08/741,639 10/28/1996 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION 08/305,366 9/13/1994 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION 08/305,366 9/13/1994 SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD SIGMA DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD FOR FORMING RECESSING ONLY TO SIGMA DELTA ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/513,95 51/13/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 18/510,951 1/21/1995 ANALOG TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/510,951		•		ANALOG TO DIGITAL CONVERTER AND METHOD OF FARRICATION
08/956,074 10/21/1997 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/743,368 10/17/1996 LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION 08/741,639 10/28/1996 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION CONSTANT DELAY LOGIC CIRCUITS AND METHOD OF FABRICATION CONSTANT DELAY LOGIC CIRCUITS AND METHOD CALIBRATION APPARATUS AND METHOD CONTROL AS ELECTROLUMINESCENT LAMPS CAPACITIVE LOADS SUCH AS CAPACITICAL CONVERTER AND METHOD OF FABRICATION CAPACITICAL CONVERT		•		
08/733,368				
11/71/996		•		
08/741,639 10/28/1996 CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION		•		LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION
08/305,366 9/13/1994 CONSTANT DELAY LOGIC CIRCUITS AND METHODS		•		CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF
08/331,015 10/28/1994 SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT CALIBRATION APPARATUS AND METHOD CALIBRATION APPARATUS AND METHOD SUCH AS ELECTROLUMINESCENT LAMPS BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS SUCH AS ELECTROLUMINESCENT LAMP BRIVER SYSTEM SURFACE MOUNTABLE INTEGRATED CIRCUIT WITH CONDUCTIVE VIAS 07/921,197 7/28/1992 BONDED WAFER PROCESSING BONDED WAFER PROC		08/205 366	0/13/1004	
CALIBRATION APPARATUS AND METHOD EFFICIENT BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS 08/490,916 6/13/1995 BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS 08/490,952 6/13/1995 BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS 08/461,643 6/5/1995 SURFACE MOUNTABLE INTEGRATED CIRCUIT WITH CONDUCTIVE VIAS OT/921,197 7/28/1992 BONDED WAFER PROCESSING 08/287,7773 8/9/1994 BONDED WAFER PROCESSING 08/299,741 9/1/1995 BONDED WAFER PROCESSING 08/299,741 9/1/1994 HIGH SPEED COMPARATOR 08/180,666 1/13/1994 VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE OT/935,765 8/26/1992 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/339,966 11/15/1994 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 5/13/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 AN		•		SIGMA-DELTA ANALOG TO DIGITAL CONVERTER WITH THREE POINT
08/490,566 6/15/1995 EFFICIENT BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS		00.001,010	10,20,1771	CALIBRATION APPARATUS AND METHOD
08/490,016 6/13/1995 BATTERY OPERATED INVERTER CIRCUIT FOR CAPACITIVE LOADS SUCH AS ELECTROLUMINESCENT LAMPS		08/490,566	6/15/1995	
BLECTROLUMINESCENT LAMPS		-		LOADS SUCH AS ELECTROLUMINESCENT LAMPS
08/490,952 6/13/1995 ELECTROLUMINESCENT LAMP DRIVER SYSTEM		08/490,016	6/13/1995	
08/461,643 6/5/1995 SURFACE MOUNTABLE INTEGRATED CIRCUIT WITH CONDUCTIVE VIAS 07/921,197 7/28/1992 BONDED WAFER PROCESSING 08/287,773 8/9/1994 BONDED WAFER PROCESSING 08/573,551 12/15/1995 BONDED WAFER PROCESSING 08/299,741 9/1/1994 HIGH SPEED COMPARATOR 08/180,666 1/13/1994 VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE 07/935,765 8/26/1992 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/339,966 11/15/1994 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION ANALOG-TO-DIG		08/400 052	6/13/1995	FLECTROLUMINESCENT LAMP DRIVER SYSTEM
17/921,197 7/28/1992 BONDED WAFER PROCESSING		,		SURFACE MOUNTABLE INTEGRATED CIRCUIT WITH CONDUCTIVE VIAS
BONDED WAFER PROCESSING				
08/573,551 12/15/1995 BONDED WAFER PROCESSING 08/299,741 9/1/1994 HIGH SPEED COMPARATOR 08/180,666 1/13/1994 VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE 07/935,765 8/26/1992 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/339,966 11/15/1994 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 5/13/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIG				
08/299,741 9/1/1994 HIGH SPEED COMPARATOR 08/180,666 1/13/1994 VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE 07/935,765 8/26/1992 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/339,966 11/15/1994 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION O8/645,139 08/645,139 5/13/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION O8/571,693 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION O8/630,874 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION O8/739,898 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION O8/885,707 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK				
METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 08/339,966 11/15/1994 METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 5/13/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		08/299,741	9/1/1994	
AND SHALLOW TRENCHES METHOD FOR FORMING RECESSED OXIDE INSOLATION CONTAINING DEEP AND SHALLOW TRENCHES 10/7/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 5/13/1996 ANALOG-TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		08/180,666	1/13/1994	VOLTAGE INDEPENDENT SYMMETRICAL CURRENT SOURCE
AND SHALLOW TRENCHES 07/785,325 10/30/1991 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/645,139 5/13/1996 ANALOG-TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		07/935,765	8/26/1992	AND SHALLOW TRENCHES
08/645,139 5/13/1996 ANALOG-TO DIGITAL CONVERTER AND METHOD OF FABRICATION 08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		08/339,966	11/15/1994	AND SHALLOW TRENCHES
08/571,693 12/31/1995 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		•		ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION
08/630,874 4/2/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		•		
08/739,898 10/30/1996 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK		•		
09/394,802 9/10/1999 ANALOG-TO-DIGITAL CONVERTER AND METHOD OF FABRICATION 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK				ANALUG-TO-DIGITAL CONVERTER AND METHOD OF FADRICATION
08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK 08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK				ANALUG-10-DIGITAL CONVERTER AND METHOD OF FARRICATION
08/885,707 6/30/1997 METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED TRENCH WITH A SINGLE MASK				METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED
THE PARTY OF THE P		08/885,707	6/30/1997	METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED
		09/345,266	6/30/1999	METHOD FOR FORMING COMPLIMENTARY WELLS AND SELF-ALIGNED

		TRENCH WITH A SINGLE MASK
08/884,726	6/30/1997	SELF-ALIGNED POWER MOSFET IN SILICON CARBIDE
07/133,902	3/25/1980	POWER MOSFET WITH AN ANODE REGION
08/975,472	11/21/1997	POWER MOSFET WITH AN ANODE REGION
08/654,316	5/28/1996	SEMICONDUCTOR PACKAGING APPARATUS AND METHOD
09/204,904	12/3/1998	SEMICONDUCTOR PACKAGING APPARATUS AND METHOD
09/014,844	1/28/1998	FOR BRUSHLESS DC MOTOR PWM SCHEME FOR QUIET COMMUTATION
09/062,964	4/20/1998	DEVICES WITH AND METHODS FOR PATTERNED WELLS
09/082,892	5/21/1998	DEFECT GATHERING BY INDUCED STRESS DEFECT GATHERING BY INDUCED STRESS
09/334,987	6/17/1999	COMPACT TRANSMISSION LINES
09/123,975	7/29/1998 8/5/1998	A LOW POWER DISCRETE SEMICONDUCTOR SURFACE MOUNT PACKAGING
09/129,321	8/3/1996	DESIGN
09/139,932	8/26/1998	PARASITIC CURRENT BARRIERS
09/183,453	10/30/1998	OVERCURRENT SENSING CIRCUIT AND SELF ADJUSTING BLANKING
09/173,111	10/15/1998	A VARIABLE FREQUENCY CLASS D MODULATOR WITH BUILT IN SOFT
00/100 070	11/19/1000	CLIPPING AND FREQUENCY LIMITING CLASS D MODULATOR WITH DIGITAL CURRENT LIMIT AND LOAD
09/183,879	11/18/1998	IMPEDANCE SENSING CIRCUITS
09/183,879	10/30/1998	START-UP CIRCUIT FOR SELF OSCILLATING CLASS D MODULATOR
09/150,429	09/09/1998	DEVICE CONTAINING SAMPLE PREPARATION SITES FOR TRANSMISSION
•		ELECTRON MICROSCOPIC ANALYSIS AND PROCESSES OF FORMATION AND USE
09/166,416	10/5/1998	SEMICONDUCTOR INTEGRATED CIRCUIT WITH TEMPORARILY
09/100,410	10/3/1998	INTERCONNECTED BOND PADS
09/358,625	7/21/1999	DOUBLY GRADED JUNCTION TERMINATION EXTENSION (JTE) FOR EDGE
,		PASSIVATION OF SEMICONDUCTOR DEVICES
09/307,879	5/10/1999	PROCESS FOR FORMING MOS-GATED DEVICES HAVING SELF-ALIGNED
		TRENCHES LOW VOLTAGE DUAL-WELL MOS TECHNOLOGY FOR ACHIEVING HIGH
09/324,553	6/3/1999	RUGGEDNESS LOWER ON-RESISTANCE AND LOWER REVERSE RECOVERED
		CHARGE
09/334,835	6/17/1999	SELF-SUPPORTED ULTRATHIN SILICON WAFER PROCESS
09/450,872	11/29/1999	EMITTER BALLAST RESISTOR WITH ENHANCED BODY EFFECT TO IMPROVE
09/430,672	11/23/1333	TH SHORT CIRCUIT WITHSTAND CAPABILITY OF POWER DEVICES
09/428,616	10/27/1999	TECHNIQUE FOR MINIMIZING GATE CHARGE AND GATE TO DRAIN
05/420,010	10/2//1999	CAPACITANCE IN POWER MOS DEVICES SUCH AS DMOS, IGBTS AND
		MOSFETS
09/339,356	6/24/1999	BACKMETAL DRAIN TERMINAL WITH LOW STRESS AND THERMAL
		RESISTANCE
09/260,411	3/1/1999	SELF-ALIGNED HIGH DENSITY TRENCH GATED DEVICE
09/342,948	6/29/1999	BRUSHLESS MULTIPASS SILICON WAFER CLEANING PROCESS FOR POST
		CHEMICAL MECHANICAL POLISHING USING IMMERSION LASER DECAPSULATION OF SEMICONDUCTOR DEVICES
09/307,896	5/10/1999	METHOD FOR FORMING A BONDED SUBSTRATE CONTAINING A PLANAR
09/255,231	2/22/1999	INTRINSIC GATHERING ZONE AND SUBSTRATE FORMED BY SAID METHOD
09/303,270	4/30/1999	POWER MOS DEVICE WITH INCREASED CHANNEL WIDTH AND PROCESS
09/303,270	4/30/1777	FOR FORMING SAME
09/283,536	4/1/1999	IMPROVED POWER TRENCH MOST GATED TRANSISTOR
09/283,531	4/1/1999	METHOD OF MAKING HIGH DENSITY POWER TRENCH MOS-GATED
03.200,000		TRANSISTOR
09/358,266	7/21/1999	USE OF A BARRIER REFRACTIVE OR ANTI-REFLECTIVE LAYER AND
		DIELELECTRIC LAYER TO IMPROVE THE LASER TRIM CHARACTERISTICS OF
		A LASER TRIMMED THIN FILM RESISTOR
09/339,274	6/23/1999	INTEGRATED RESISTIVE CONTRACT
09/343,845	6/30/1999	IMPROVED SOLDERABILITY OF PLATED ELECTRONIC TERMINATIONS
09/345,261	6/30/1999	METHOD FOR MAKING A DIFFUSED BACK-SIDE LAYER ON A BONDED -
02/2 (0,200		WAFER WITH A THICK BOND OXIDE
09/350,575	7/9/1999	IMPROVED DENSE MCT USING SELF-ALIGNED SILICIDATION WITH
00/210 224	E /0 E /1 0 0 0	COMPLEX SPACERS TRENCH-GATED DEVICE HAVING TRENCH WALLS FORMED BY SELECTIVE
09/318,334	5/25/1999	EPITAXIAL GROWTH AND PROCESS FOR FORMING DEVICE
09/344,856	6/28/1999	A NOVEL METHOD OF FILM ETCH/REMOVAL VERIFICATION IN AN IN-SITU
03/344,030	0.20.1777	ETCH AND DEPOSITION
09/314,323	5/19/1999	MOS GATED POWER DEVICE HAVING EXTENDED TRENCH AND DOPING
,		ZONE AND PROCESS FOR FORMING SAME
09/345,930	7/1/1999	POWER SEMICONDUCTOR MOUNTING PACKAGE CONTAINING BALL GRID
00/014.05=	C/00/1000	ARRAY POTTED TRANSDUCER ARRAY WITH MATCHING NETWORK IN A TWO OR
09/344,867	6/28/1999	MORE PASS CONFIGURATION
09/344,868		EDGE TERMINATION FOR SILICON DEVICES
U 21 .244.0U0	6/28/1000	FIGE TERMINATION FOR BILICON DEVICES
•	6/28/1999 7/1/1999	LOW TEMPERATURE COEFFICIENT RESISTOR (TCRL)
09/345,929 09/342,376	6/28/1999 7/1/1999 6/29/1999	LOW TEMPERATURE COEFFICIENT RESISTOR (TCRL) DUAL MODE CLASS D AMPLIFIERS

09/367,325	8/11/1999	CO-PATTERNING THIN FILM RESISTORS OF DIFFERENT COMPOSITIONS WITH A CONDUCTIVE HARD MASK AND METHOD FOR SAME
09/437,678	11/10/1999	LOW NOSIE LOW DISTORTION CLASS D AMPLIFIER
09/437,393	11/10/1999	CLASS D AMPLIFIER WITH BANDWIDTH INDEPENDENT OF LOAD IMPEDANCE
09/438,210	11/12/1999	CLASS D MODULATOR WITH PEAK CURRENT LIMIT AND LOAD IMPEDANCE SENSING CIRCUITS
09/442,291	11/19/1999	BACKWARDS DRIVABLE MOS OUTPUT DRIVER